

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

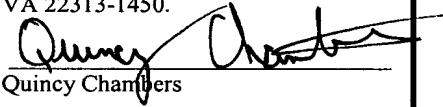
In re Application of: Seung Ki Joo et al.  
Serial No.: 10/038,990  
Filing Date: January 4, 2002  
Examiner: Thao X. Le  
Group Art Unit: 2814  
Title: *Method of Fabricating a Semiconductor Device  
Including a Crystalline Active Silicon Layer*

**MAIL STOP RCE**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

Dear Sir:

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Quincy Chambers

NOTIFICATION OF EXTENSION OF TIME UNDER 37 C.F.R. §1.136

Applicants hereby take an Extension of Time for responding to the Final Office  
Action dated April 23, 2003, for three (3) months from July 23, 2003 to October 23, 2003.

<u>    </u> First Month	\$ 55.00
<u>    </u> Second Month	\$ 210.00
<u>  X  </u> Third Month	\$ 475.00

Total: \$ 475.00

A check in an amount of \$475.00 made payable to the Commissioner of Patents and  
Trademarks is attached hereto.

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475.00 30

The Commissioner is hereby authorized to charge any deficiency or credit any overpayment to Deposit Account No. 02-0384 of BAKER BOTTS L.L.P.

Respectfully submitted,

BAKER BOTTS L.L.P.



Jay B. Johnson  
Attorney for Applicants  
Reg. No. 38,193

October 21, 2003

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**05073**